

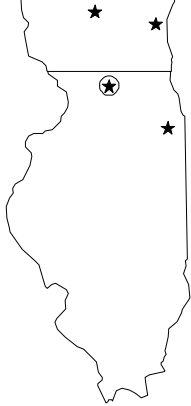


# DIGITAL SIGNAL PROCESSING



## DSP SEMINAR

Website: [www.dspseminar.org](http://www.dspseminar.org)



**OCTOBER 29, 1998**  
**CLOCKTOWER RESORT & CONFERENCE CENTER**  
**ROCKFORD, ILLINOIS**

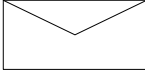
**SPONSORED BY IEEE SECTIONS OF: ROCK RIVER VALLEY**  
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**A ONE-DAY SEMINAR ON DSP ALGORITHMS, TOOLS, TECHNIQUES AND APPLICATIONS. ALSO FEATURES A PROFESSIONAL ACTIVITIES TRACK.**

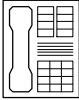
	Track 1	Track 2	Track 3	Professional Activities
7:30-8:30	Registration and Continental Breakfast			
8:30-10:00	<b>DSP Architectures For Motor Controls</b> Jeff Stafford/TI	<b>Bridging the Implementation Gap - A New Approach to FPGA Based DSP Design</b> Mark Paluszkiwicz/Xilinx	<b>DSP Fundamentals</b> Dr. Gary Blank/Consultant	<b>Secure IP Distribution and Delivery</b> Peter Feist/Gatefield
10:15-12:00	<b>DSP Architectures For Motor Controls</b> (cont.) Jeff Stafford/TI	<b>Bridging the Implementation Gap</b> (cont.) Mark Paluszkiwicz/Xilinx	<b>Real-Time DSP - System Design and Implementation</b> Prof. Sen Kuo/NIU	<b>6 Sigma Defined and Deployed</b> Jack Sherman/Motorola
12:00-1:00	Lunch (hot roast beef sandwich, salad, mashed potatoes, broccoli, dessert, beverage; vegetarian alternative available)			
1:00-2:45	<b>DSP for Sensorless Motor Controls</b> Prof. Bob Lorenz/UofW-Mad.	<b>Standard SW Infrastructure for Fixed-Point DSPs</b> Robert Matusiak/TI	<b>An Overview of New and Emerging EDA Technologies for DSP Design</b> Rajiv Jain/HP	<b>Employee Personal Liability</b>
3:00-4:30	<b>DSP for Sensorless Motor Controls</b> (cont.) Prof. Bob Lorenz/UofW-Mad.	<b>Techniques &amp; Tools for FPGA Based DSP Design</b> Mark Paluszkiwicz/Xilinx	<b>Fixed Point Architectures and Implementation of DSP Algorithms</b> Chuck Brokish/TI	<b>Consulting – An Exciting Way To Practice Engineering</b> Dr. Gary Blank/Consultant
4:30-4:45	Drawing for 900MHz Cordless Phone (return fully completed survey to qualify)			



**THREE EASY WAYS TO REGISTER**

  
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the Registration Form (see back)

  
**On-Line**  
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Track 1	Track 2	Track 3	Prof. Act.
1	1	1	1
2	2	2	2
3	3	3	3
4	4	4	4

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**Gloria Burton**  
**c/o Sundstrand Aerospace**  
**P.O. Box 7002 MS 190-6**  
**Rockford, IL 61125**

*All refund requests must be made in writing by mail to seminar management and be received no later than October 9, 1998. Mail refund requests to the above address.*

*No-shows will be liable for the full registration fee.*

## CIRCLE APPROPRIATE REGISTRATION FEE

### After October 9, 1993

	IEEE Member	Non-Member	Student Member	Student Non-Member
All Day	\$100	\$125	\$20	\$25
1/2 Day	\$75	\$100	\$10	\$15

**Hotel Rooms:** Special rates available at the ClockTower (800-358-7666) until October 15.

**Meals:** Continental Breakfast and Lunch will be provided for attendees of the All-Day session only.

**Directions:** Take I-90 to Rockford, get off at the Business 20 exit. At the stoplight at end of the exit, go straight into the ClockTower Resort and Conference Center. Convention rooms are around the left (east) side of the complex.

**Program changes:** The IEEE DSP Seminar program content is subject to change should unforeseeable circumstances arise with respect to individual speakers/topics



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